

What is claimed are:

1. A sense amplifier, comprising:
a sensing unit for sensing data stored at a memory cell;
5 a reference voltage generator having a reference cell, for generating a reference voltage;
an equalizer that makes same the output of the sensing unit and the output of the reference voltage generator before a word line of the memory cell is enabled, and is then disabled; and
10 a comparator for comparing the output of the sensing unit and the output of the reference voltage generator.
2. The sense amplifier as claimed in claim 1, wherein the equalizer consists of a switch driven by an equalizer enable signal.
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3. The sense amplifier as claimed in claim 1, wherein the equalizer consists of a PMOS transistor driven by the equalizer enable signal.
4. The sense amplifier as claimed in claim 1, wherein the equalizer
20 consists of an NMOS transistor driven by the equalizer enable signal.